

RT2P29M

Composite Transistor With Resistor
For Switching Application
Silicon PNP Epitaxial Type

DESCRIPTION

RT2P29M is composite transistor with built-in bias resistor.

FEATURE

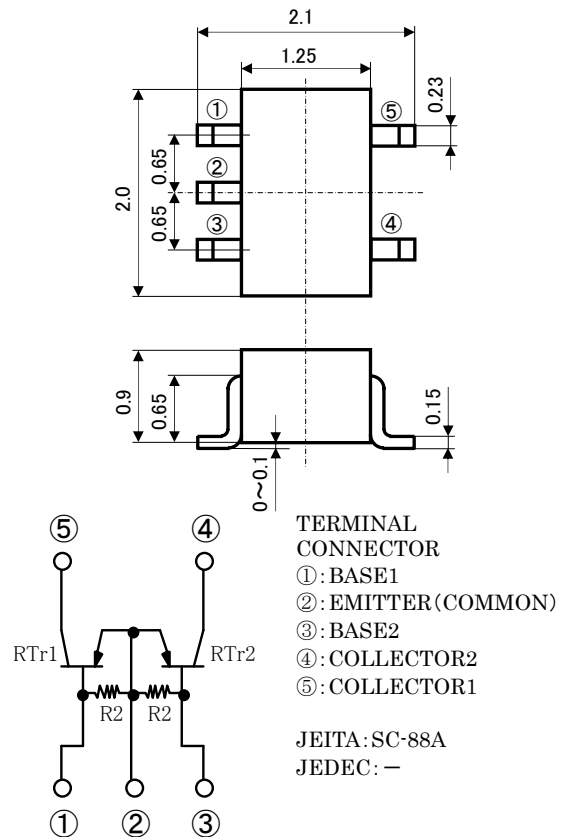
Built-in bias resistor (R2=100kΩ)
Mini package for easy mounting

APPLICATION

Inverted circuit, Switching circuit,
Interface circuit, Driver circuit

OUTLINE DRAWING

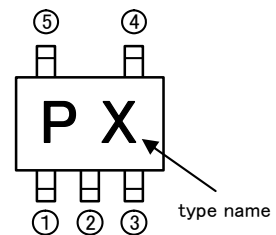
Unit: mm



MAXIMUM RATING (Ta=25°C) (RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-6	V
VCEO	Collector to Emitter voltage	-50	V
IC	Collector current	-100	mA
ICM	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
Tstg	Storage temperature	-55 ~ +150	°C

MARKING



ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1, RTr2 COMMON)

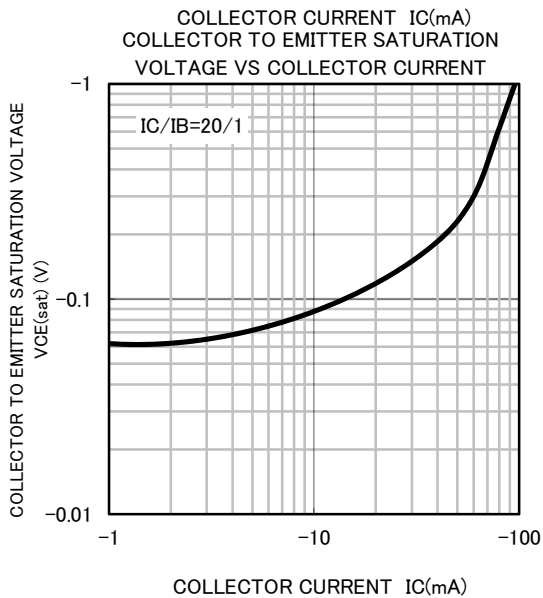
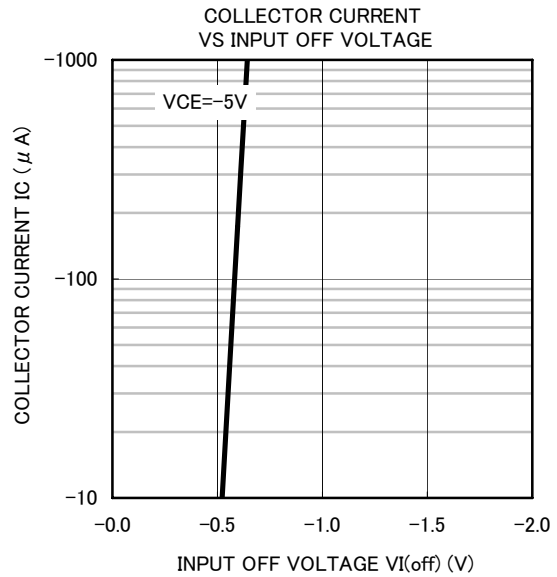
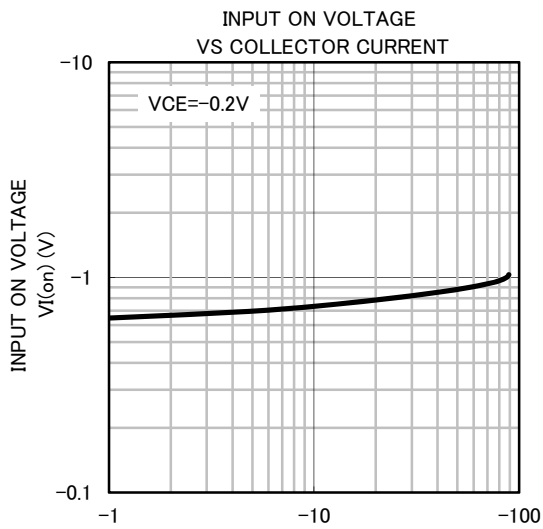
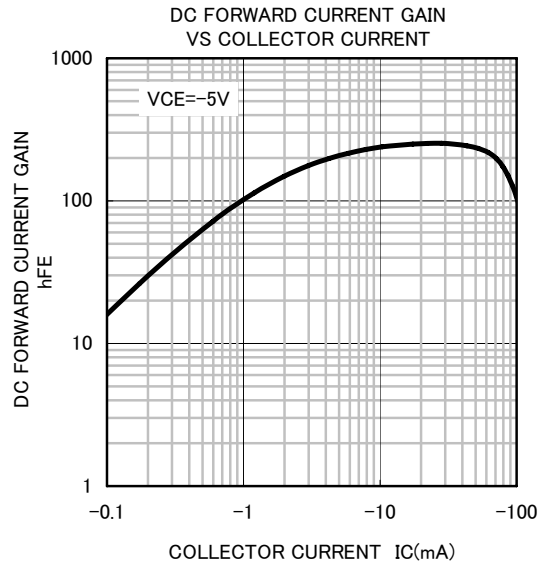
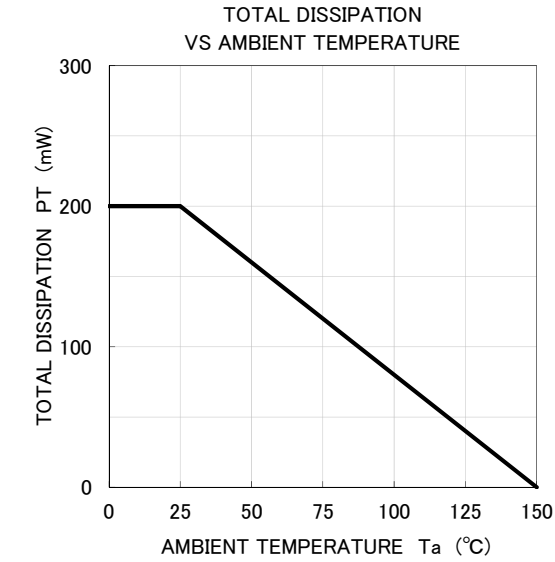
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
V(BR)CEO	Collector to Emitter break down voltage	IC=-100 μA, RBE=∞	-50	—	—	V
ICBO	Collector cut off current	VCE=-50V, IE=0	—	—	-0.1	μA
IEBO	Emitter cut off current	VEB=-5V, IC=0	-38	-50	-73	μA
hFE	DC forward current gain	VCE=-5V, IC=-5mA	82	—	—	—
VCE(sat)	Collector to Emitter saturation voltage	IC=-10mA, IB=-0.5mA	—	—	-0.3	V
R2	Emitter to Base resistor	—	70	100	130	kΩ
fT	Gain band width product	VCE=-6V, IE=10mA	—	150	—	MHz

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TYPICAL CHARACTERISTICS

($T_a=25^\circ\text{C}$)(R_{Tr1}, R_{Tr2} COMMON)





Keep safety first in your circuit designs!

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