

RT2P14M

Composite Transistor With Resistor
For Switching Application
Silicon PNP Epitaxial Type

DESCRIPTION

RT2P14M is composite transistor with built-in bias resistor.

FEATURE

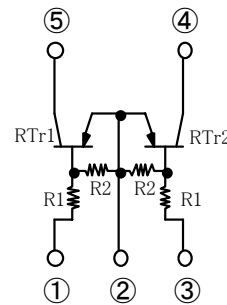
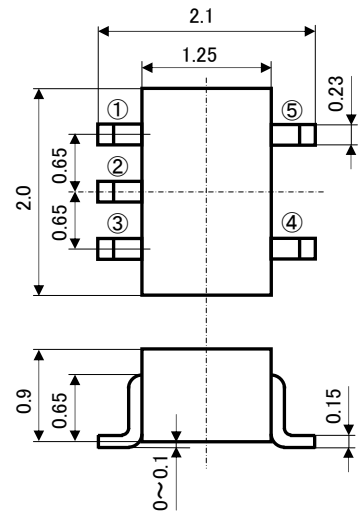
Built-in bias resistor (R1=10k Ω , R2=47k Ω)
Mini package for easy mounting

APPLICATION

Inverted circuit, Switching circuit,
Interface circuit, Driver circuit

OUTLINE DRAWING

Unit: mm



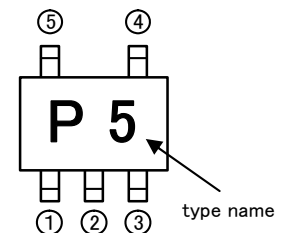
TERMINAL CONNECTOR
①: BASE1
②: EMITTER (COMMON)
③: BASE2
④: COLLECTOR2
⑤: COLLECTOR1

JEITA: SC-88A
JEDEC: -

MAXIMUM RATING (Ta=25°C) (RT1, RT2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-6	V
VCEO	Collector to Emitter voltage	-50	V
VIN	Input voltage	-40	V
IC	Collector current	-100	mA
ICM	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
Tstg	Storage temperature	-55~+150	°C

MARKING



ELECTRICAL CHARACTERISTICS (Ta=25°C) (RT1, RT2 COMMON)

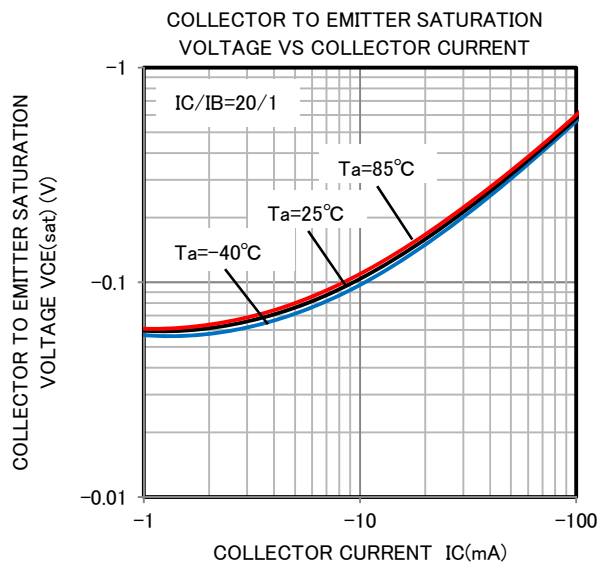
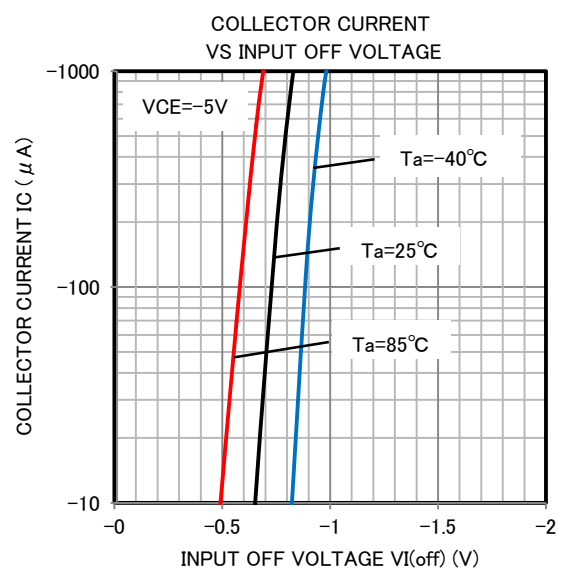
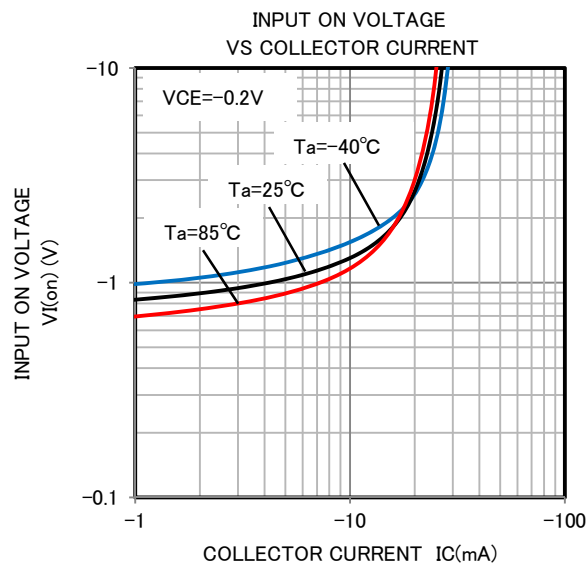
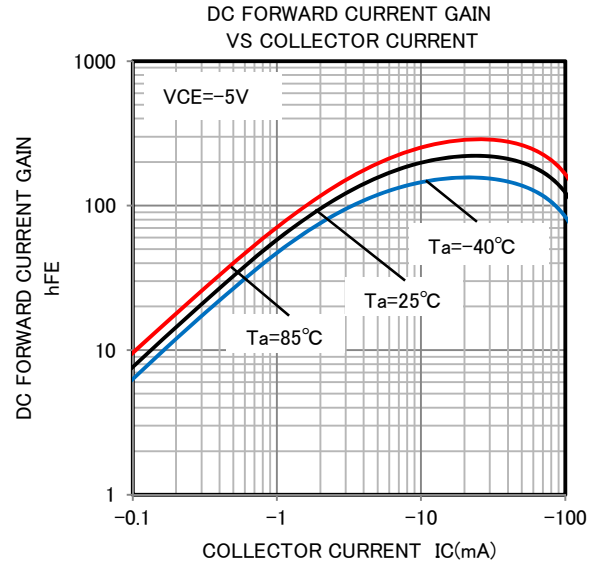
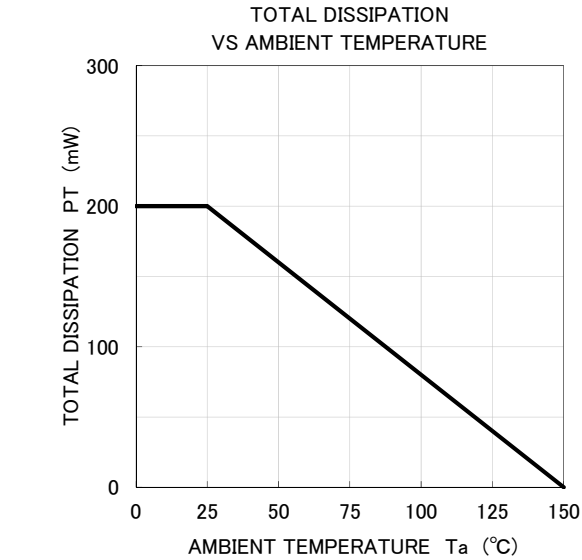
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
V(BR)CEO	Collector to Emitter breakdown voltage	IC=-100 μ A, RBE= ∞	-50	-	-	V
ICBO	Collector cut off current	VCE=-50V, IE=0	-	-	-0.1	μ A
IEBO	Emitter cut off current	VEB=-5V, IC=0	-70	-88	-119	μ A
hFE	DC forward current gain	VCE=-5V, IC=-5mA	50	-	-	-
VCE(sat)	Collector to Emitter saturation voltage	IC=-10mA, IB=-0.5mA	-	-0.1	-0.3	V
V(ON)	Input on voltage	VCE=-0.2V, IC=-5mA	-	-1.0	-1.8	V
V(OFF)	Input off voltage	VCE=-5V, IC=-100 μ A	-0.4	-0.7	-	V
R1	Input resistor	-	7	10	13	k Ω
R2/R1	Resistor ratio	-	4.2	4.7	5.1	-
fT	Gain band width product	VCE=-6V, IE=10mA	-	150	-	MHz

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TYPICAL CHARACTERISTICS

(RT_{r1}, RT_{r2} COMMON)





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