

RT1P141U-T150

Transistor With Resistor
For Switching Application
Silicon PNP Epitaxial Type

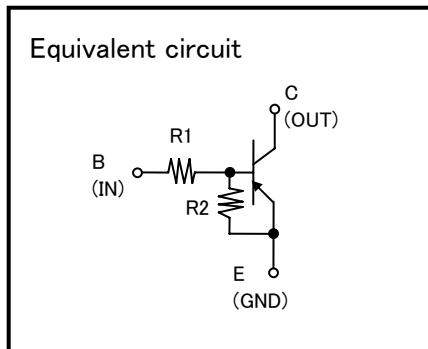
AEC-Q101 Compliance

FEATURE

- Built-in bias resistor (R1=10kΩ, R2=10kΩ)
- Mini package for easy mounting

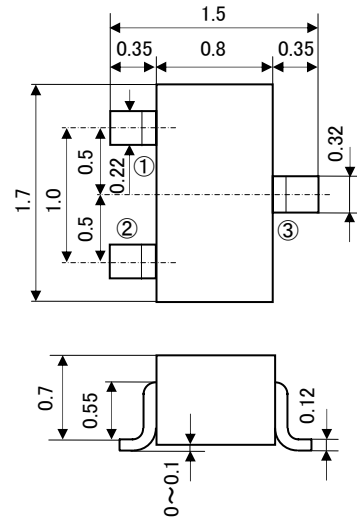
APPLICATION

Inverted circuit, switching circuit, interface circuit, driver circuit.



OUTLINE DRAWING

UNIT : mm



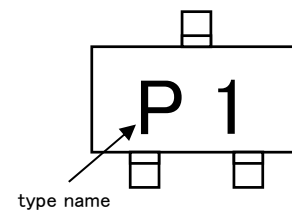
Terminal
Connector

- ①: Base JEITA: SC-75A
②: Emitter JEDEC: —
③: Collector

MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
V _{CBO}	Collector to Base voltage	-50	V
V _{EBO}	Emitter to Base voltage	-10	V
V _{CEO}	Collector to Emitter voltage	-50	V
V _{IN}	Input voltage	-40	V
I _C	Collector current	-100	mA
I _{CM}	Peak Collector current	-200	mA
P _C	Collector dissipation	150	mW
T _j	Junction temperature	+150	°C
T _{stg}	Storage temperature	-55~+150	°C

MARKING



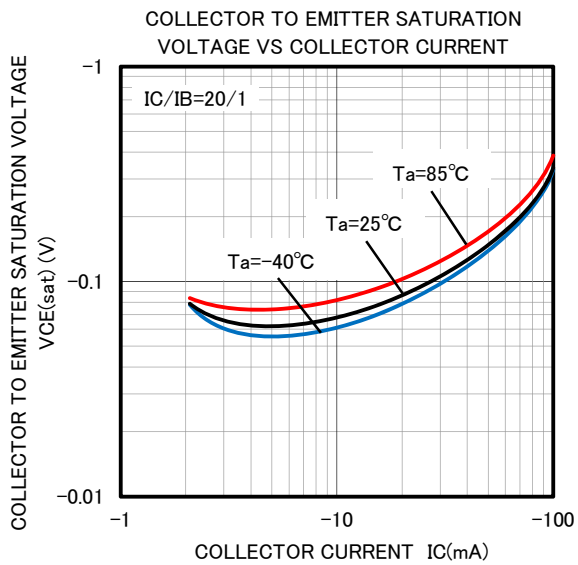
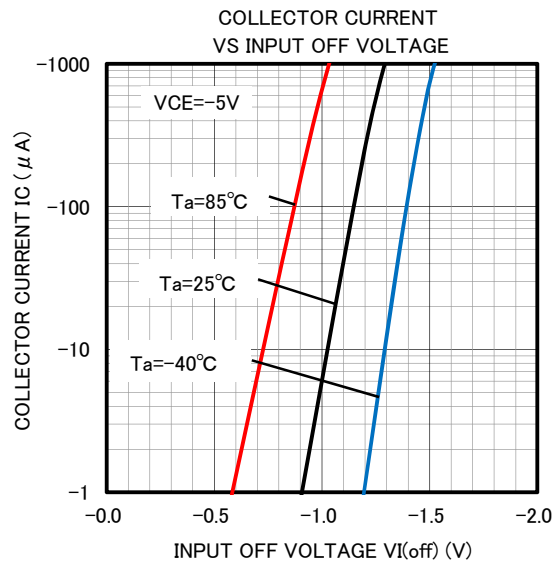
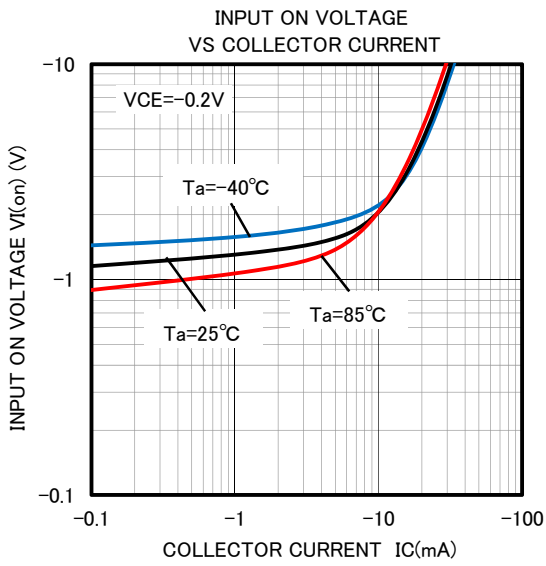
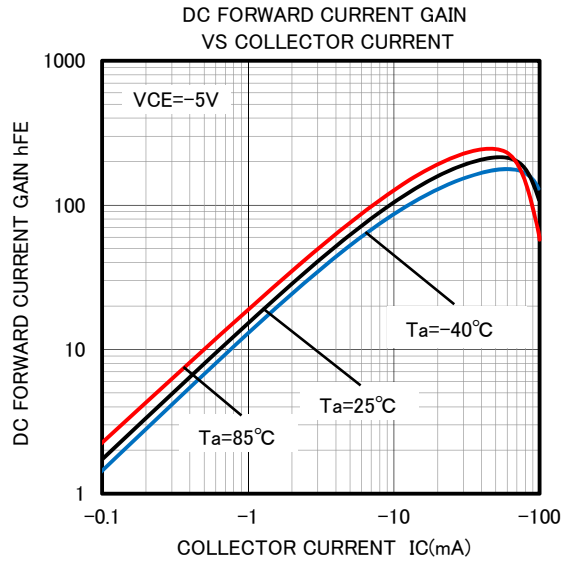
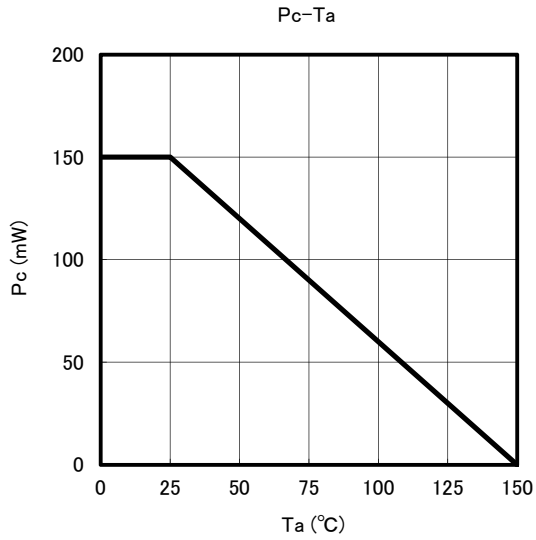
ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	
V _{(BR)CEO}	C to E breakdown voltage	I _C = -100 μA, R _{BE} = ∞	-50	—	—	V
I _{CBO}	Collector cut off current	V _{CB} = -50V, I _E = 0	—	—	-0.1	μA
I _{EBO}	Emitter cut off current	V _{EB} = -5V, I _C = 0	-192	-250	-357	μA
h _{FE}	DC forward current gain	V _{CE} = -5V, I _C = -10mA	50	—	—	—
V _{CE(sat)}	C to E saturation voltage	I _C = -10mA, I _B = -0.5mA	—	-0.1	-0.3	V
V _{I(ON)}	Input on voltage	V _{CE} = -0.2V, I _C = -5mA	—	-1.5	-3.0	V
V _{I(OFF)}	Input off voltage	V _{CE} = -5V, I _C = -100 μA	-0.8	-1.1	—	V
R1	Input resistor	—	7	10	13	kΩ
R2/R1	Resistor ratio	—	0.9	1.0	1.1	—
f _T	Gain band width product	V _{CE} = -6V, I _E = 10mA	—	150	—	MHz

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TYPICAL CHARACTERISTICS





Keep safety first in your circuit designs!

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