INK0001AC1-T150

High speed switching
Silicon N-channel MOSFET

AEC-Q101 Compliance

DESCRIPTION

INK0001AC1 is a Silicon N-channel MOSFET.
This product is most suitable for low voltage use such as portable machinery, because of low voltage drive and low resistance.

FEATURE

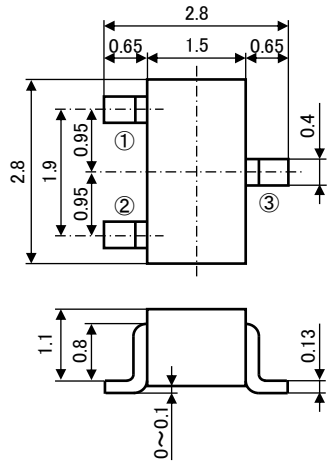
・Input impedance is high, and not necessary to consider a drive electric current.
・Drive voltage 2.5V
・Low on Resistance.
$R_{DS(ON)}$=3.5Ω(TYP) @I_D=100mA,V_{GS}=4.0V
・High speed switching.
・Small packing for easy mounting.

APPLICATION

Inductive loads switching

OUTLINE DRAWING

Unit:mm



TERMINAL CONNECTOR
①:GATE
②:SOURCE
③:DRAIN

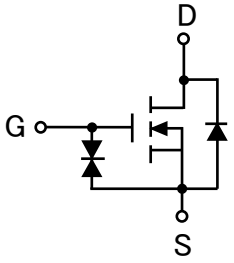
JEITA－:SC-59
JEDEC:Similar to TO-236

MAXIMUM RATINGS(Ta=25℃)

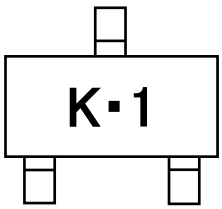
Parameter	Symbol	Rating	Unit
Drain-Source voltage	V_{DSS}	50	V
Gate-Source voltage	V_{GSS}	±8	V
Drain current(DC)	I_D	100	mA
Drain current(Pulse)	I_{DP}	400(※1)	mA
Total power dissipation	P_D	200	mW
Channel temperature	T_{ch}	+150	℃
Storage temperature	T_{stg}	−55～+150	℃

※1:Pw≦10μs, Duty cycle≦1%

EQUIVALENT CIRCUIT



MARKING

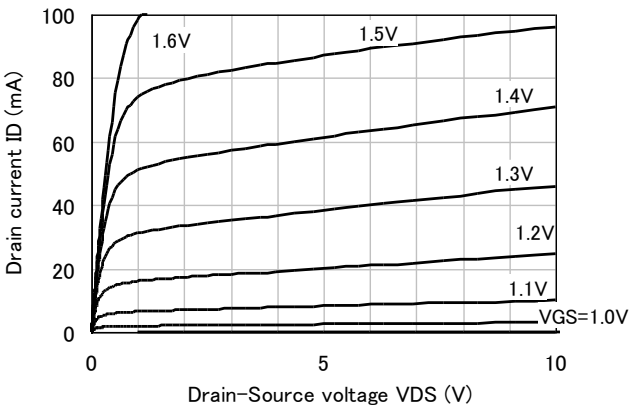


ELECTRICAL CHARACTERISTICS(Ta=25℃)

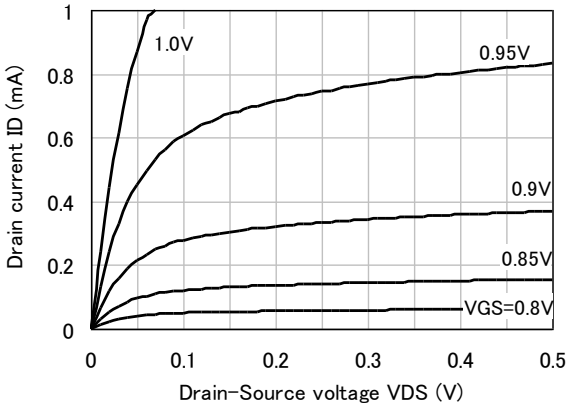
Parameter	Symbol	Test Condition	Limit			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	I_D=100μA, V_{GS}=0V	50	−	−	V
Gate-Source leak current	I_{GSS}	V_{GS}=±5V, V_{DS}=0V	−	−	±0.5	μA
Zero gate voltage drain current	I_{DSS}	V_{DS}=50V, V_{GS}=0V	−	−	1.0	μA
Gate threshold voltage	V_{th}	I_D=250μA, V_{DS}=V_{GS}	0.6	−	1.2	V
Forward transfer admittance	$\|Y_{fs}\|$	V_{DS}=10V, I_D=0.1A	−	250	−	mS
Static Drain-Source on-state resistance	$R_{DS(ON)}$	I_D=100mA, V_{GS}=4.0V	−	3.5	−	Ω
Input capacitance	C_{iss}	V_{DS}=10V, V_{GS}=0V,f=1MHz	−	24	−	pF
Output capacitance	C_{oss}		−	5	−	
Switching time	t_{on}	V_{DD}=5V , I_D=10mA V_{GS}=0～5V	−	11	−	ns
	t_{off}		−	50	−	

TYPICAL CHARACTERISTICS

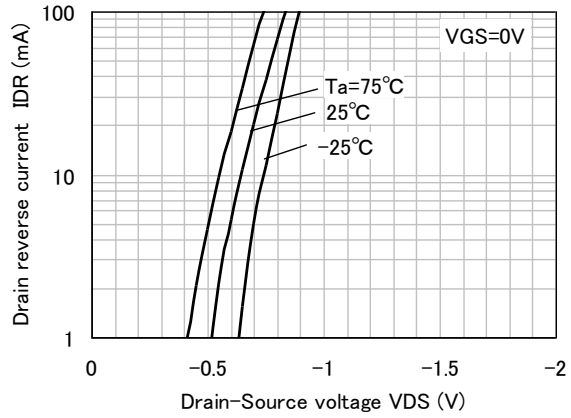
Ta=25℃ ID – VDS



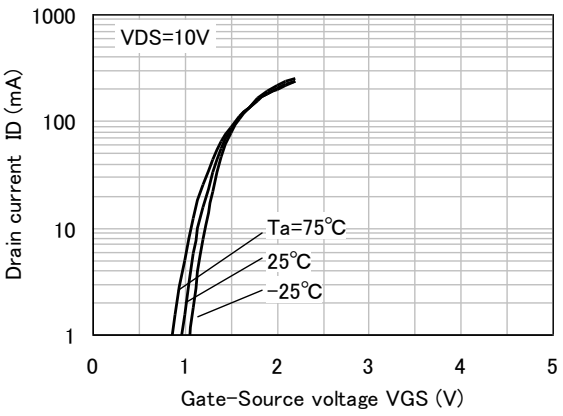
Ta=25℃ ID – VDS(Low voltage region)



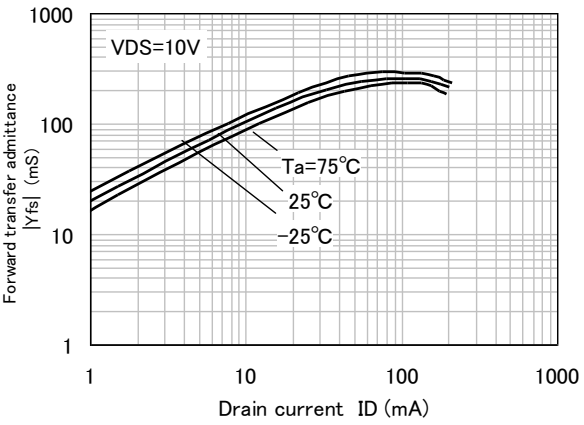
IDR – VDS



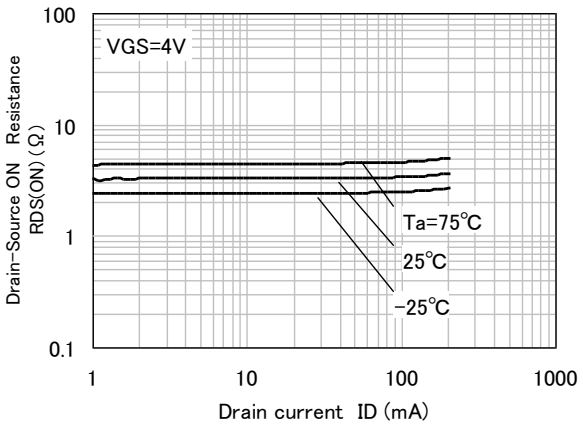
ID – VGS



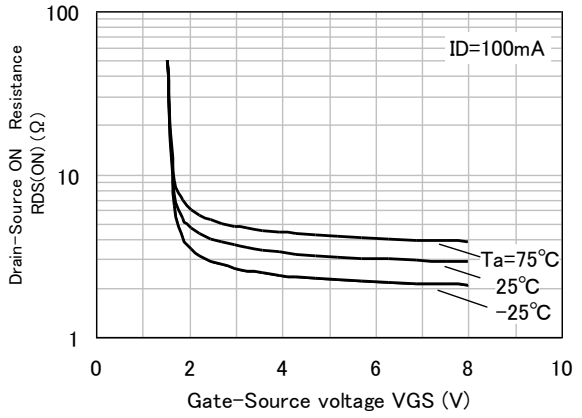
|Yfs| – ID



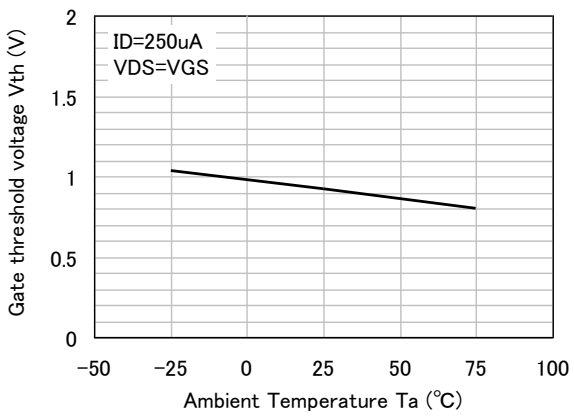
RDS(ON) – ID



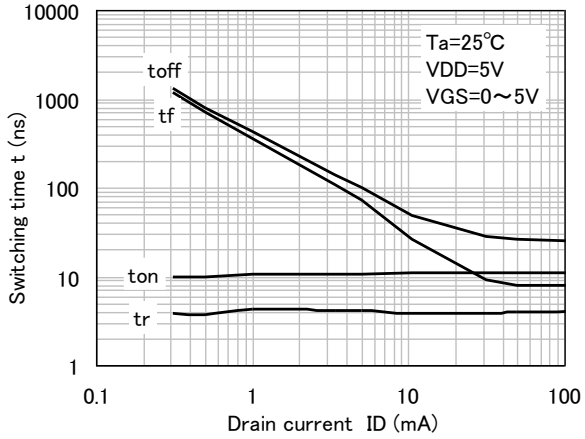
RDS(ON) - VGS



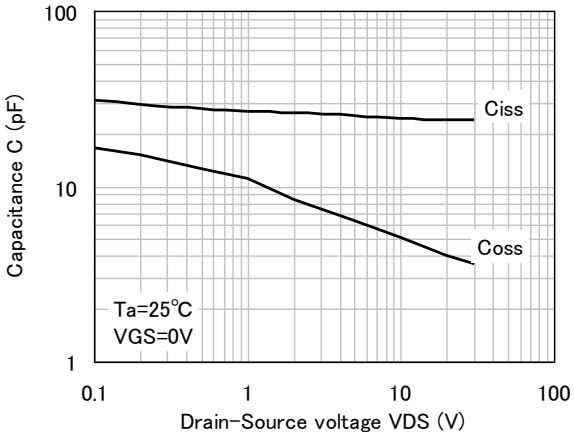
Vth - Ta



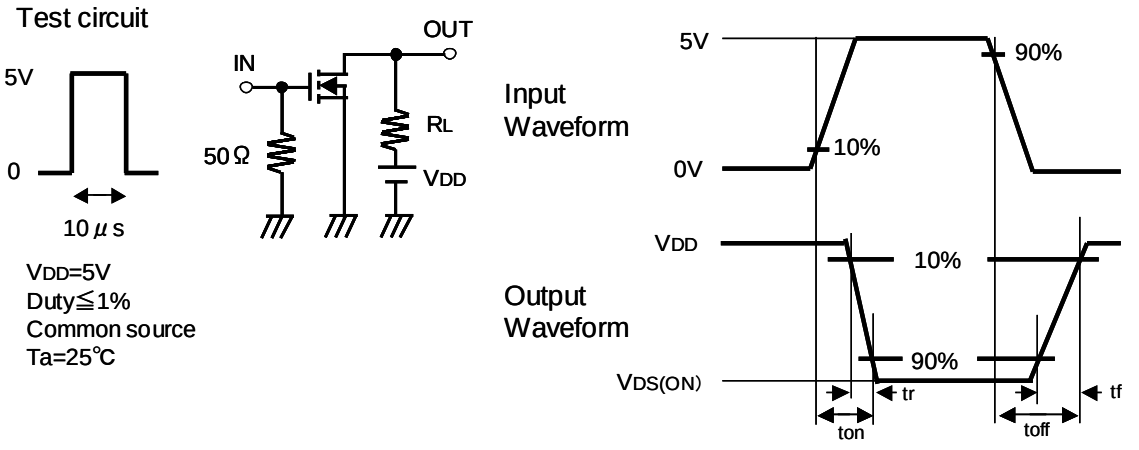
t - ID



C - VDS



Switching time test condition



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